



FGP20N60UFD

600V, 20A Field Stop IGBT

Features

- High current capability
- Low saturation voltage: $V_{CE(sat)} = 1.8V @ I_C = 20A$
- High input impedance
- Fast switching
- RoHS compliant

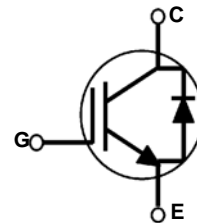
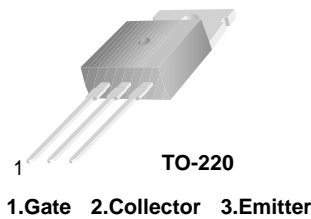
Applications

- Induction Heating, UPS, SMPS, PFC



General Description

Using Novel Field Stop IGBT Technology, Fairchild's new series of Field Stop IGBTs offer the optimum performance for Induction Heating, UPS, SMPS and PFC applications where low conduction and switching losses are essential.



Absolute Maximum Ratings

Symbol	Description	Ratings	Units
V_{CES}	Collector to Emitter Voltage	600	V
V_{GES}	Gate to Emitter Voltage	± 20	V
I_C	Collector Current @ $T_C = 25^\circ C$	40	A
	Collector Current @ $T_C = 100^\circ C$	20	A
$I_{CM} (1)$	Pulsed Collector Current @ $T_C = 25^\circ C$	60	A
P_D	Maximum Power Dissipation @ $T_C = 25^\circ C$	165	W
	Maximum Power Dissipation @ $T_C = 100^\circ C$	66	W
T_J	Operating Junction Temperature	-55 to +150	$^\circ C$
T_{stg}	Storage Temperature Range	-55 to +150	$^\circ C$
T_L	Maximum Lead Temp. for soldering Purposes, 1/8" from case for 5 seconds	300	$^\circ C$

Notes:
1: Repetitive rating: Pulse width limited by max. junction temperature

Thermal Characteristics

Symbol	Parameter	Typ.	Max.	Units
$R_{\theta JC}(IGBT)$	Thermal Resistance, Junction to Case	-	0.76	$^\circ C/W$
$R_{\theta JC}(Diode)$	Thermal Resistance, Junction to Case	-	2.51	$^\circ C/W$
$R_{\theta JA}$	Thermal Resistance, Junction to Ambient	-	62.5	$^\circ C/W$

Package Marking and Ordering Information

Device Marking	Device	Package	Packaging Type	Qty per Tube	Max Qty per Box
FGP20N60UFD	FGP20N60UFDTU	TO-220	Tube	50ea	-

Electrical Characteristics of the IGBT T_C = 25°C unless otherwise noted

Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Units
Off Characteristics						
V_{CES}	Collector to Emitter Breakdown Voltage	$V_{GE} = 0V, I_C = 250\mu A$	600	-	-	V
$\frac{\Delta V_{CES}}{\Delta T_J}$	Temperature Coefficient of Breakdown Voltage	$V_{GE} = 0V, I_C = 250\mu A$	-	0.6	-	V/°C
I_{CES}	Collector Cut-Off Current	$V_{CE} = V_{CES}, V_{GE} = 0V, T_C = 25^\circ C$	-	-	250	μA
		$V_{CE} = V_{CES}, V_{GE} = 0V, T_C = 125^\circ C$	-	-	1	mA
I_{GES}	G-E Leakage Current	$V_{GE} = V_{GES}, V_{CE} = 0V$	-	-	±400	nA
On Characteristics						
$V_{GE(th)}$	G-E Threshold Voltage	$I_C = 250\mu A, V_{CE} = V_{GE}$	4.0	5.0	6.5	V
$V_{CE(sat)}$	Collector to Emitter Saturation Voltage	$I_C = 20A, V_{GE} = 15V$	-	1.8	2.4	V
		$I_C = 20A, V_{GE} = 15V, T_C = 125^\circ C$	-	2.0	-	V
Dynamic Characteristics						
C_{ies}	Input Capacitance	$V_{CE} = 30V, V_{GE} = 0V, f = 1MHz$	-	940	-	pF
C_{oes}	Output Capacitance		-	110	-	pF
C_{res}	Reverse Transfer Capacitance		-	40	-	pF
Switching Characteristics						
$t_{d(on)}$	Turn-On Delay Time	$V_{CC} = 400V, I_C = 20A, R_G = 10\Omega, V_{GE} = 15V, \text{Inductive Load}, T_C = 25^\circ C$	-	13	-	ns
t_r	Rise Time		-	17	-	ns
$t_{d(off)}$	Turn-Off Delay Time		-	87	-	ns
t_f	Fall Time		-	32	64	ns
E_{on}	Turn-On Switching Loss		-	0.38	-	mJ
E_{off}	Turn-Off Switching Loss		-	0.26	-	mJ
E_{ts}	Total Switching Loss	-	0.64	-	mJ	
$t_{d(on)}$	Turn-On Delay Time	$V_{CC} = 400V, I_C = 20A, R_G = 10\Omega, V_{GE} = 15V, \text{Inductive Load}, T_C = 125^\circ C$	-	13	-	ns
t_r	Rise Time		-	16	-	ns
$t_{d(off)}$	Turn-Off Delay Time		-	92	-	ns
t_f	Fall Time		-	63	-	ns
E_{on}	Turn-On Switching Loss		-	0.41	-	mJ
E_{off}	Turn-Off Switching Loss		-	0.36	-	mJ
E_{ts}	Total Switching Loss	-	0.77	-	mJ	
Q_g	Total Gate Charge	$V_{CE} = 400V, I_C = 20A, V_{GE} = 15V$	-	63	-	nC
Q_{ge}	Gate to Emitter Charge		-	7	-	nC
Q_{gc}	Gate to Collector Charge		-	32	-	nC

Electrical Characteristics of the Diode $T_C = 25^\circ\text{C}$ unless otherwise noted

Symbol	Parameter	Test Conditions	Min.	Typ.	Max	Units	
V_{FM}	Diode Forward Voltage	$I_F = 10\text{A}$	$T_C = 25^\circ\text{C}$	-	1.9	2.5	V
			$T_C = 125^\circ\text{C}$	-	1.7	-	
t_{rr}	Diode Reverse Recovery Time	$I_{ES} = 10\text{A}, dI_{ES}/dt = 200\text{A}/\mu\text{s}$	$T_C = 25^\circ\text{C}$	-	35	-	ns
			$T_C = 125^\circ\text{C}$	-	57	-	
Q_{rr}	Diode Reverse Recovery Charge	$I_{ES} = 10\text{A}, dI_{ES}/dt = 200\text{A}/\mu\text{s}$	$T_C = 25^\circ\text{C}$	-	41	-	nC
			$T_C = 125^\circ\text{C}$	-	96	-	

Typical Performance Characteristics

Figure 1. Typical Output Characteristics

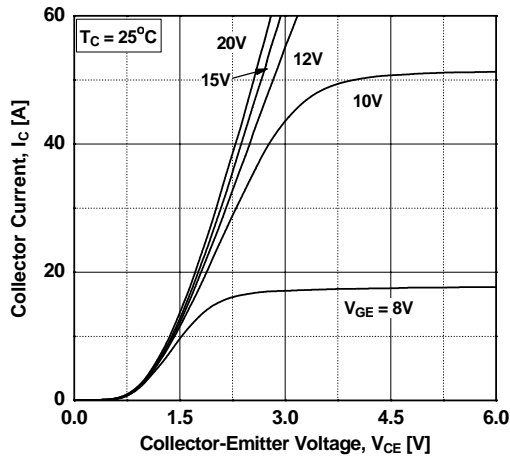


Figure 2. Typical Output Characteristics

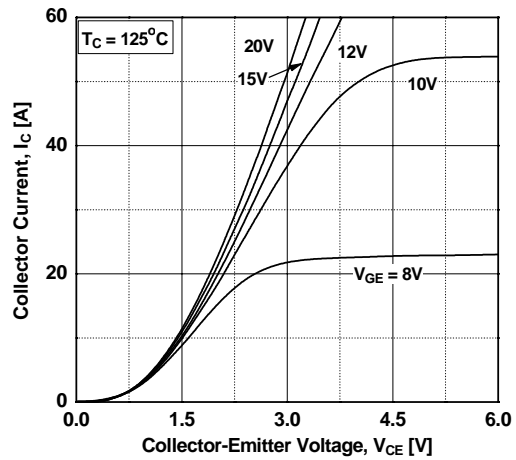


Figure 3. Typical Saturation Voltage Characteristics

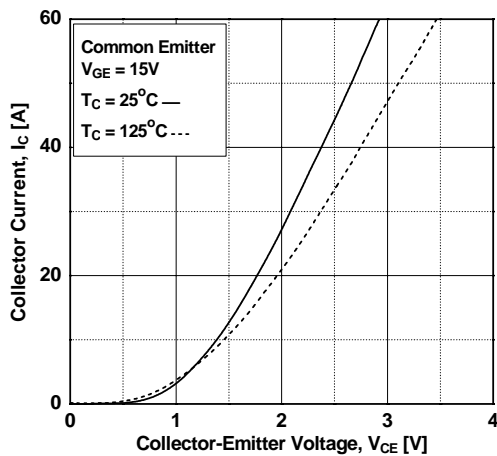


Figure 4. Transfer Characteristics

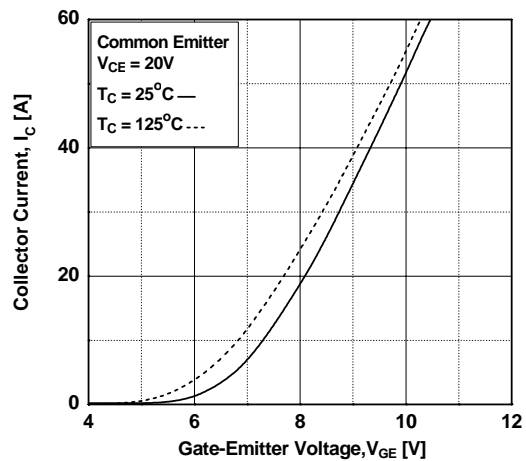


Figure 5. Saturation Voltage vs. Case Temperature at Variant Current Level

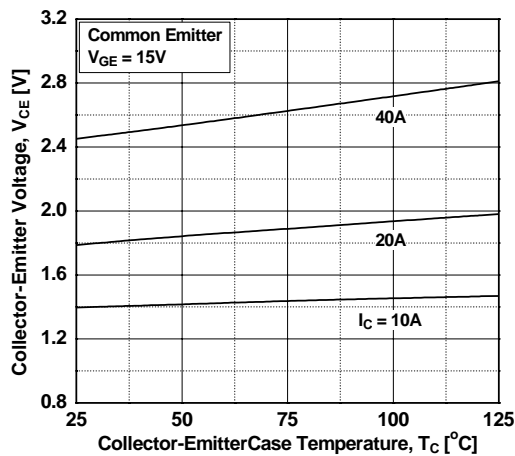
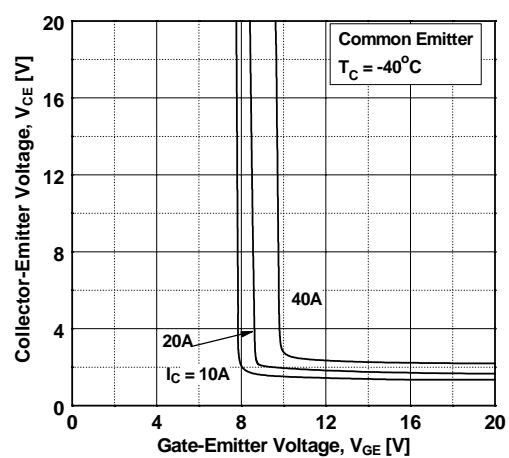


Figure 6. Saturation Voltage vs. Vge



Typical Performance Characteristics

Figure 7. Saturation Voltage vs. V_{GE}

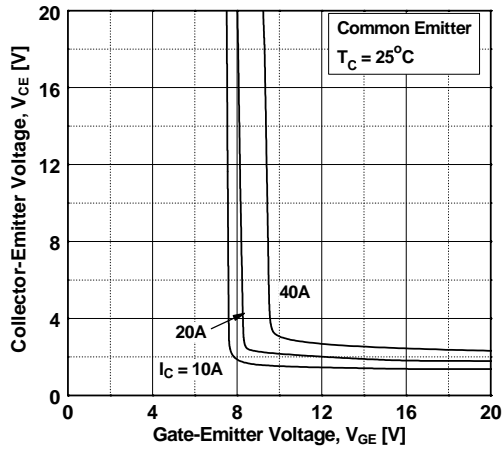


Figure 8. Saturation Voltage vs. V_{GE}

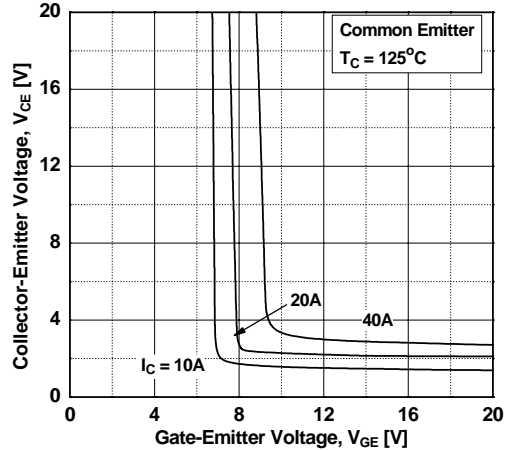


Figure 9. Capacitance Characteristics

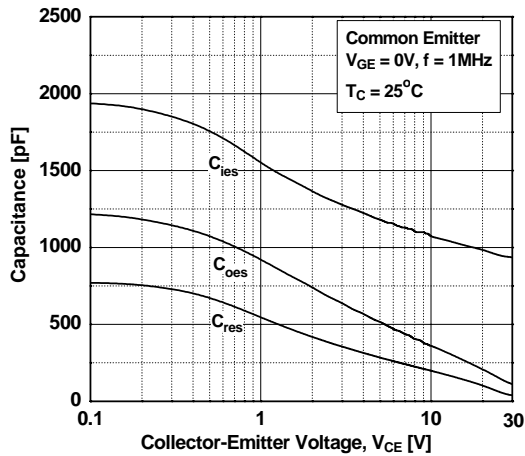


Figure 10. Gate charge Characteristics

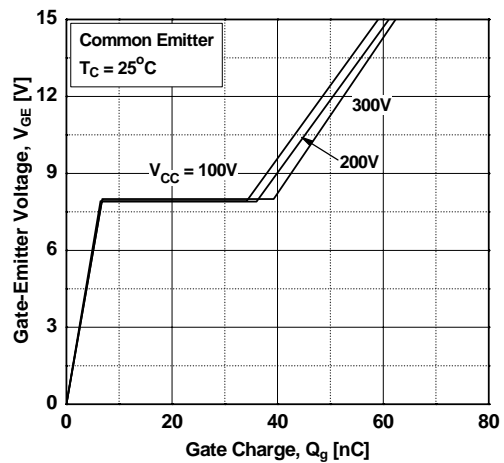


Figure 11. SOA Characteristics

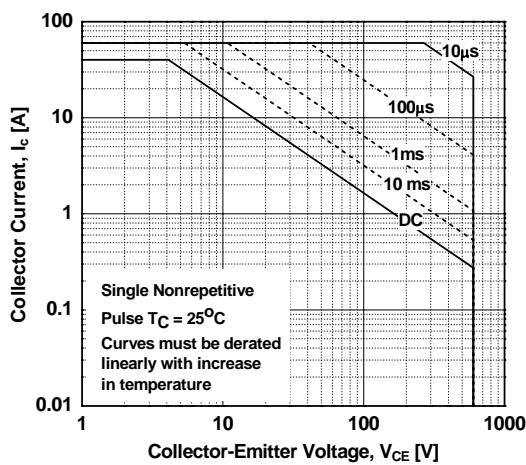
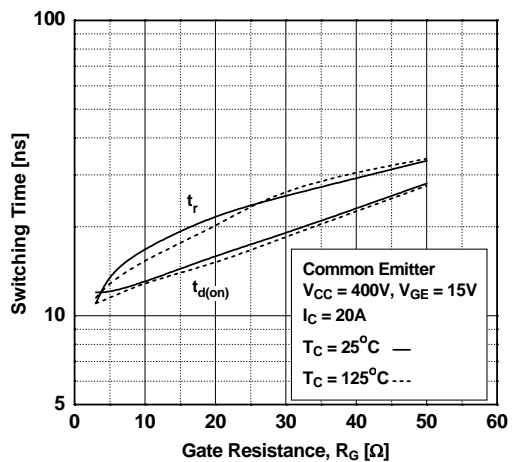


Figure 12. Turn-on Characteristics vs. Gate Resistance



Typical Performance Characteristics

Figure 13. Turn-off Characteristics vs. Gate Resistance

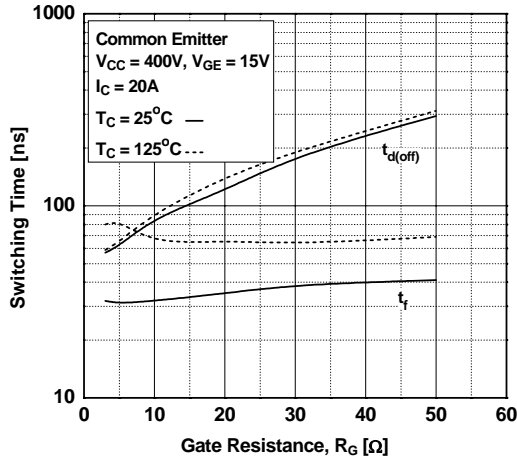


Figure 14. Turn-on Characteristics vs. Collector Current

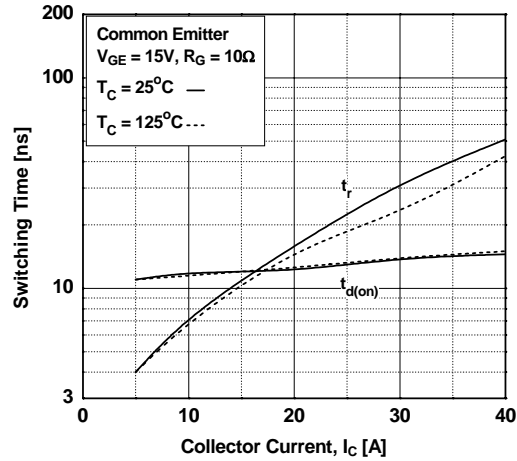


Figure 15. Turn-off Characteristics vs. Collector Current

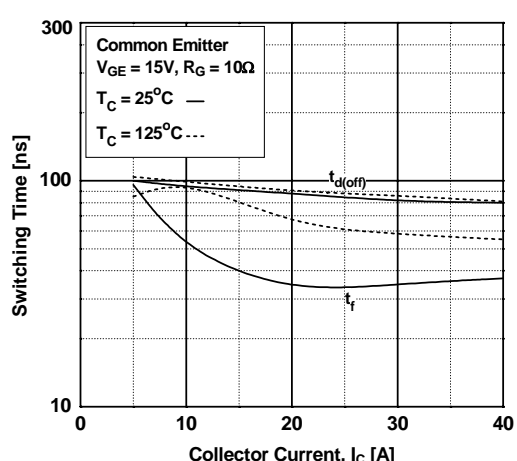


Figure 16. Switching Loss vs. Gate Resistance

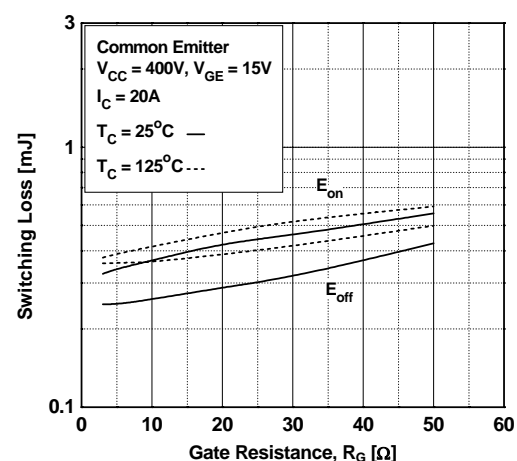


Figure 17. Switching Loss vs. Collector Current

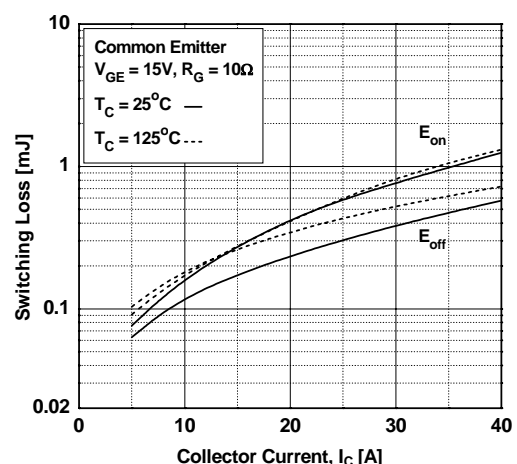
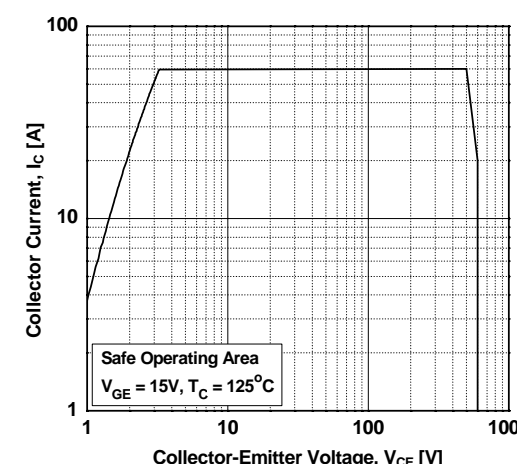


Figure 18. Turn off Switching SOA Characteristics



Typical Performance Characteristics

Figure 19. Forward Characteristics

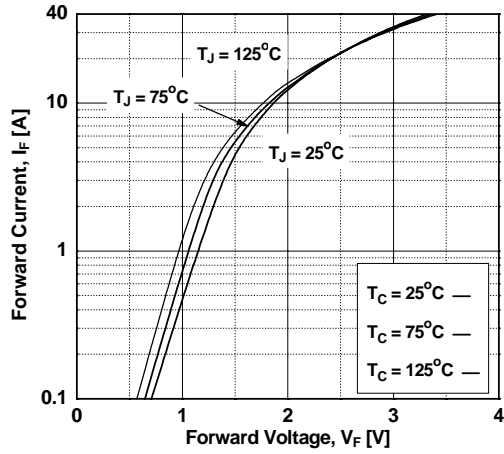


Figure 20. Reverse Current

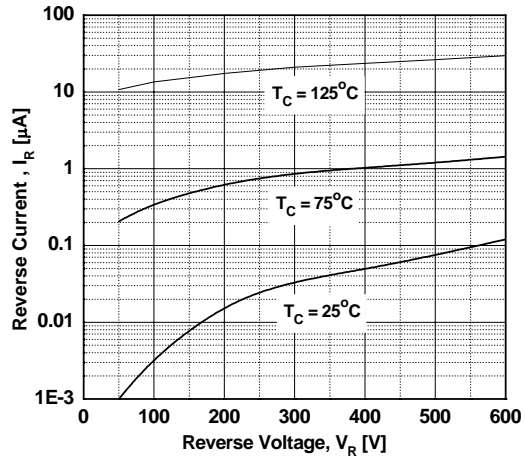


Figure 21. Stored Charge

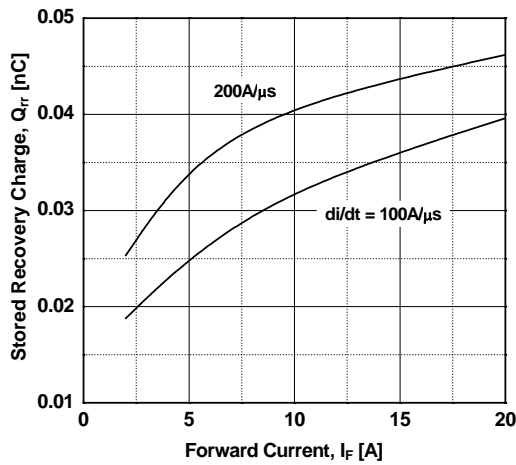


Figure 22. Reverse Recovery Time

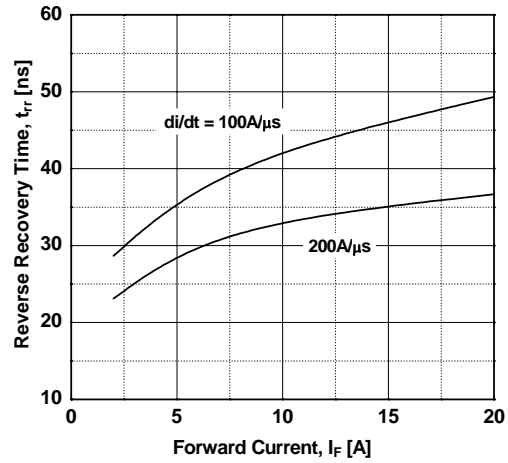
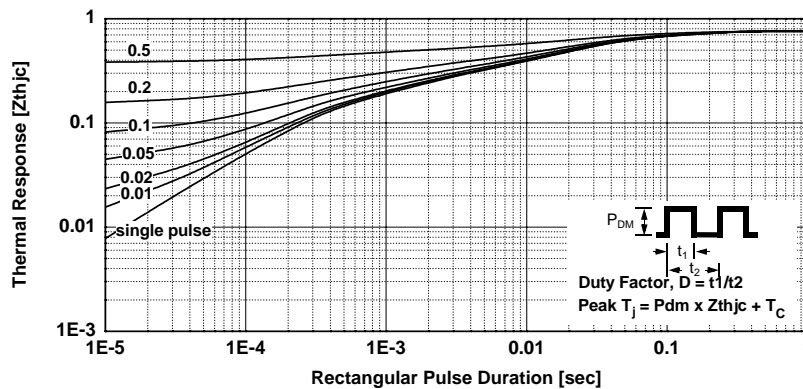
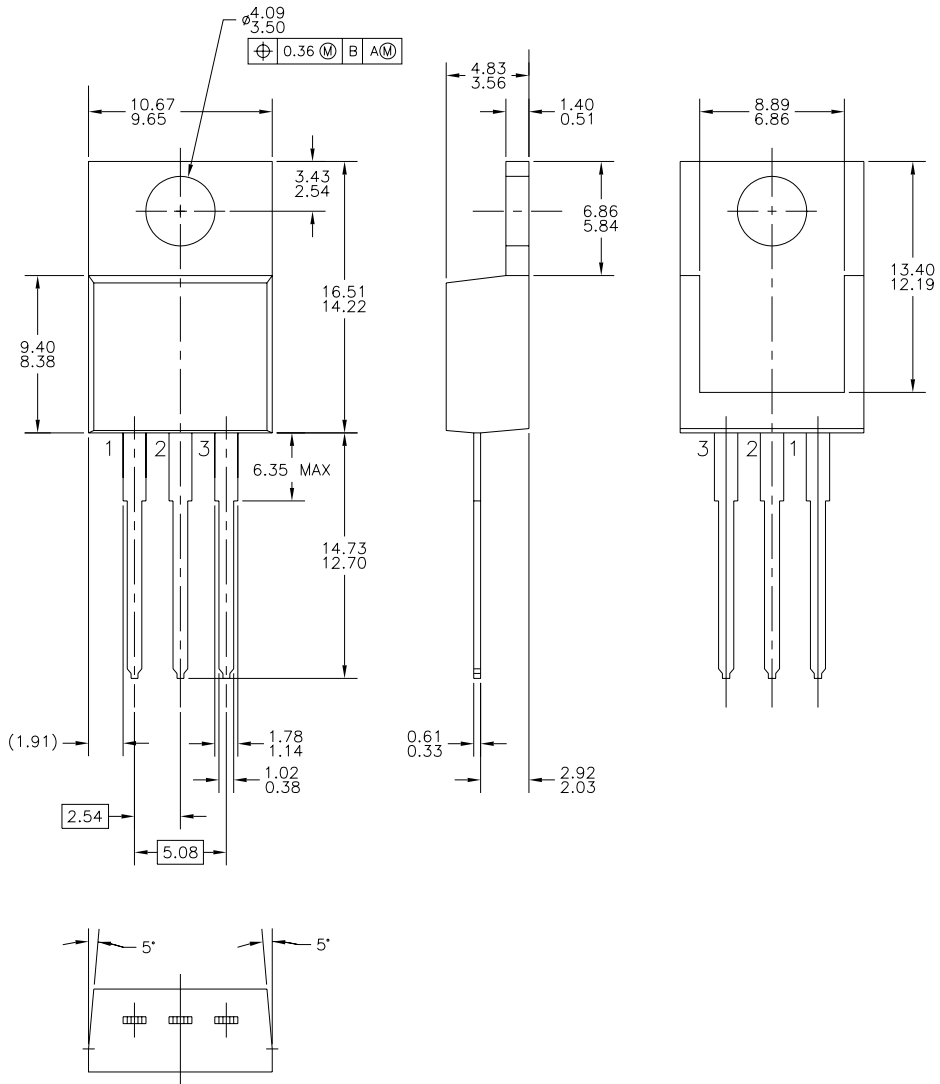


Figure 23. Transient Thermal Impedance of IGBT



Mechanical Dimensions






TO-220





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Definition of Terms

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Preliminary	First Production	Datasheet contains preliminary data; supplementary data will be published at a later date. Fairchild Semiconductor reserves the right to make changes at any time without notice to improve design.
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